



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Docket No: TI-31026

Jonathan Brodsky, et al.

Art Unit:

2814

Serial No: 09/668,999

Examiner:

Dana Farahani

Filed: September 25, 2000

June 11, 2002

Confirmation No. 3740

Date: 🔒

For:

CIRCUIT AND METHOD FOR AN INTEGRATED CHARGED DEVICE MODEL CLAMP

## NOTICE OF APPEAL FROM THE PRIMARY EXAMINER TO THE BOARD OF APPEALS

**Assistant Commissioner for Patents** Washington, DC 20231

Dear Sir:

MAILING CERTIFICATE UNDER 37 C.F.R. § 1.8(a) I hereby certify that the above correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, DC 20231 on \_

Applicants hereby appeal to the Board of Appeals from the decision of the Primary Examiner mailed May 29, 2002, finally rejecting Claims 1-24.

	The i	tem(s) checked below are appropriate:
1.		An extension of time to respond to the final rejection
		was granted on is requested for month.
2.		A timely response to the final rejection has been filed.
3.	<u>X</u>	Fee \$320.00:
		Not required (Fee paid in prior appeal)  The Commissioner of Patents is hereby authorized to charge any fees which may be required or credit any overpayment to the deposit account of Texas Instruments Incorporated, Account No. 20-0668.

This form is submitted in triplicate.

06/18/2002 AWDNDAF1 00000139 200668 01 FC:119

320.00 CH

**Bennis Moore Attorney for Appellants** Reg. No. 28,885

Texas Instruments Incorporated P.O. Box 655474, MS 3999 Dallas, TX 75265 (972) 917-5646

09668999